

PCI Express® 6.x CoppoLink® Internal Cable Interposer



Key Features

- Quick and simple set-up
- Allows Capturing and decoding of all PCIe traffic between host and device
- Supports NVMe, PCIe and CXL protocols
- Supports CoppoLink SFF-TA-1016/MCIO mechanical specification
- Supports data rates of 2.5 GT/s, 5.0 GT/s, 8.0 GT/s, 16.0 GT/s, 32.0 GT/s, and 64.0 GT/s
- Supports SMBus
- Supports: BP_TYPE, CWAKE#, 2W_CLK, 2W_DATA, PERST#, CPRSNT# sideband signals
- Environmental:
 - Temperature (operating): 5° to 40°C (41° to 104°F)
 - Temperature (non-operating): -20° to 60°C (-4° to 140° F)
 - Humidity (operating): 5% to 80% RH (non-condensing) at <=30°C, 50% max RH (non-condensing) at 40°C
 - Humidity (non-operating): 5% to 95% max RH (non-condensing)
- Dimensions:
225.89mm x 128.59mm x 37.34mm (8.89" x 5.063" x 1.470")

Teledyne LeCroy's PCIe 6.x CoppoLink Internal Cable Interposer enables engineers to test product designs that incorporate card edge connectors or cabled connector assemblies that utilize the PCI-SIG CoppoLink mechanical specification based on the SFF-TA-1016 connector (same as Mini Cool Edge I/O – MCIO connector) with PCIe® 6.x, NVM Express (NVMe) or Compute Express Link™ (CXL) technologies.

The CoppoLink internal connector is a flexible, robust, and cost-effective connector and cable assembly solution that help server and networking equipment design flexibility, reduce overall space, and extend the reach for high data rate signals. The CoppoLink Interposer provides probing capability up to 16 lanes of PCIe 6.x traffic. The interposer has two 74-pin MCIO connectors on each side and can be configured to connect to 38-pin, 124-pin and 148-pin MCIO connectors using adapter cables.

These connectors based on the SFF- TA-1016 pin out provide a probing point for CoppoLink cables. Summit protocol analyzers can capture and record bus activity on CoppoLink applications using this interposer.

The CoppoLink Interposer Card works in combination with a Summit Protocol Analyzer to monitor, capture, and record PCIe traffic on CoppoLink connectors connected between host connector and device side connectors for protocol analysis. The interposer supports data rates of 2.5 GT/s, 5.0 GT/s, 8.0 GT/s, 16.0

GT/s, 32GT/s, and 64GT/s, and supports link widths up to x16 using standard or MultiPort configurations.

CoppoLink MCIO connectors and cabled connector assemblies are used extensively in data center networking equipment, Server/ Storage devices, High Performance Computing (HPC) and Switches/Routers and other equipment. Now, design and test engineers can test, identify issues, and optimize CoppoLink connector/cable solutions using the Teledyne LeCroy PCIe 6.x CoppoLink Cable interposer in combination with one of the Summit PCIe protocol analyzers.

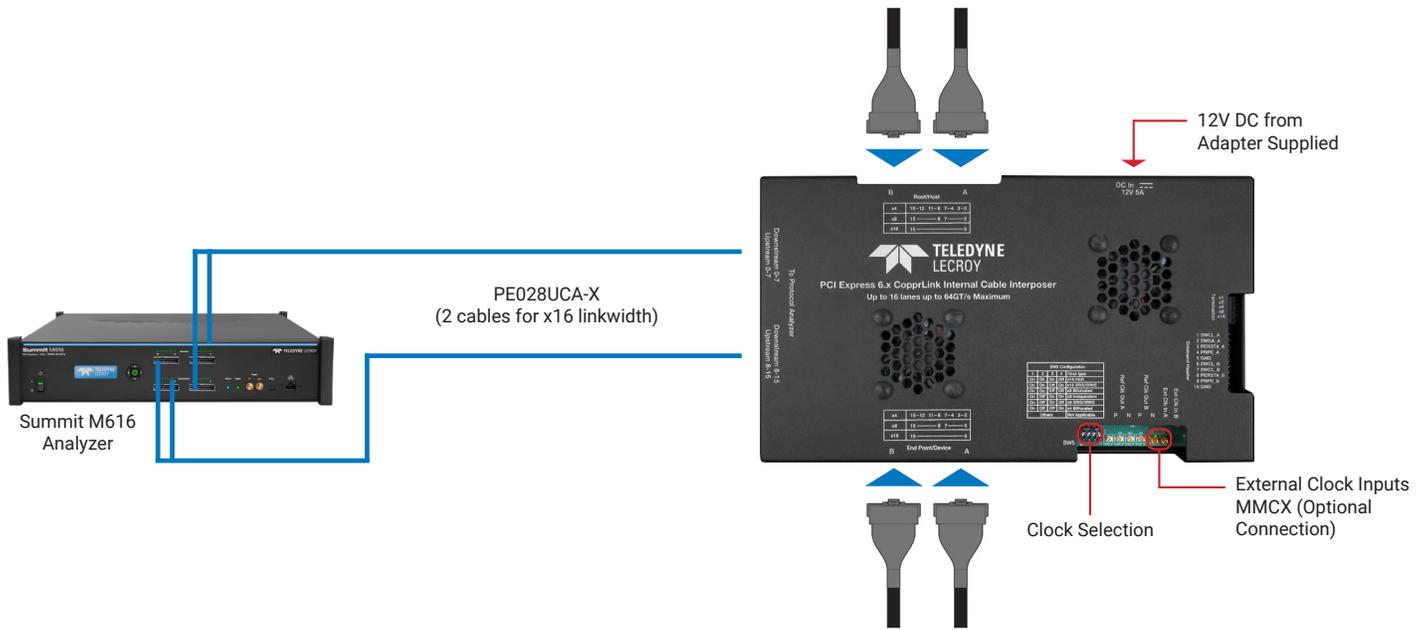
PCI EXPRESS®

nvm EXPRESS®

CXL Compute Express Link®



PCIe 6.x CopprLink Internal Cable Interposer Connection Overview



PCIe 6.x CopprLink Interposer Analyzer and Cable Compatibility		
	Analyzer Platform	Cable Requirement(s)
	Summit T48 Protocol Analyzer (Standard)	PE031UCA-X (Y-Cable)
	Summit T48 Protocol Analyzer (Multiport)	PE030UCA-X (Straight Cable)
	Summit T416 Protocol Analyzer (Standard)	PE031UCA-X (Y-Cable)
	Summit T416 Protocol Analyzer (Multiport)	PE030UCA-X (Straight Cable)
	Summit T54 Protocol Analyzer (G5 PHY)	PE027UCA-X (Straight Cable)
	Summit M5x Protocol Analyzer	PE029UCA-X (Y-Cable)
	Summit T516 Protocol Analyzer (Standard)	PE028UCA-X (Y-Cable)
	Summit T516 Protocol Analyzer (Multiport)	PE027UCA-X (Straight Cable)
	Summit M64 Protocol Analyzer	PE027UCA-X (Straight Cable)
	Summit M616 Protocol Analyzer (Standard)	PE028UCA-X (Y-Cable)
	Summit M616 Protocol Analyzer (Multiport)	PE027UCA-X (Straight Cable)

Ordering Information

Product Description

PCIe 6.x CopprLink Internal Cable Interposer

Product Code

PE6316UIA-X



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1-800-5-LeCroy • teledynelecroy.com



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