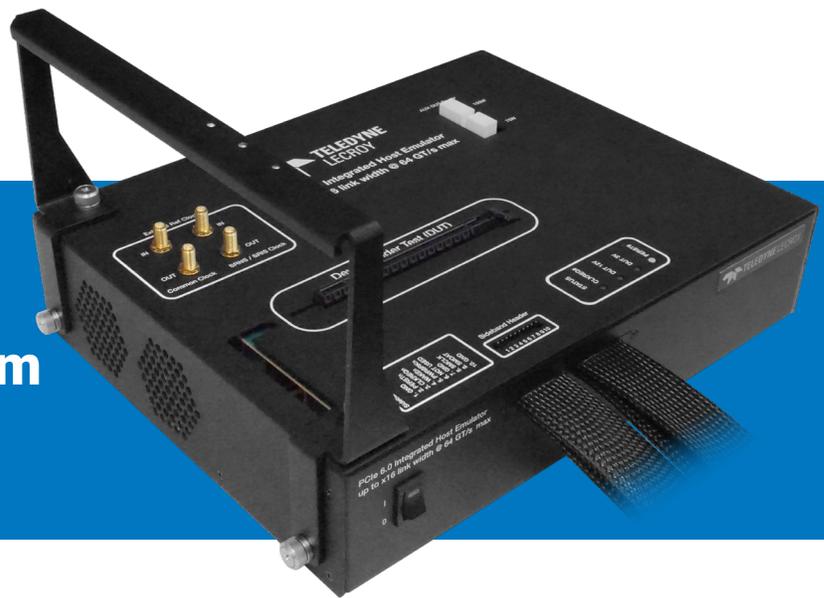# PCI Express® 6.0 Host Emulator Platform
for Summit™ M616

Key Features

- Accessory to Summit M616 Analyzer/ Exerciser allows test and debug of PCI Express cards by providing host emulation

- Supports PCIe 6.0 (all standard data rates up to 64.0 GT/s)

- Supports link widths of x1, x2, x4, x8 and x16 for CEM

- Supports CLKREQ# and SRIS

- Provides mechanical support and power for both the Summit M616 Analyzer/ Exerciser and the device under test (DUT)

- Allows for use of internal or external reference clocks

- Sideband Signals
  - PERST\CLKREQ
  - WAKE
  - PWRBRK
  - SMCLK
  - SMDAT

- Supports 3v/ 12V DUT power

- Dimensions:
  304mm x 235mm x 67mm
  (11.97" x 9.25" x 2.63)

- Weight:
  5.8 lbs

- Power Requirements:
  AC 100-240V, 50Hz, 400W

Teledyne LeCroy's PCI Express 6.0 Host Emulator platform for the Summit M616 Protocol Analyzer/Exerciser provides a convenient, powerful and flexible test platform for PCI Express devices at data rates up to 64.0 GT/s and link widths up to x16. The Host Emulator Platform can be configured to behave as a root complex or endpoint, enabling extensive protocol-level testing of PCIe® devices.

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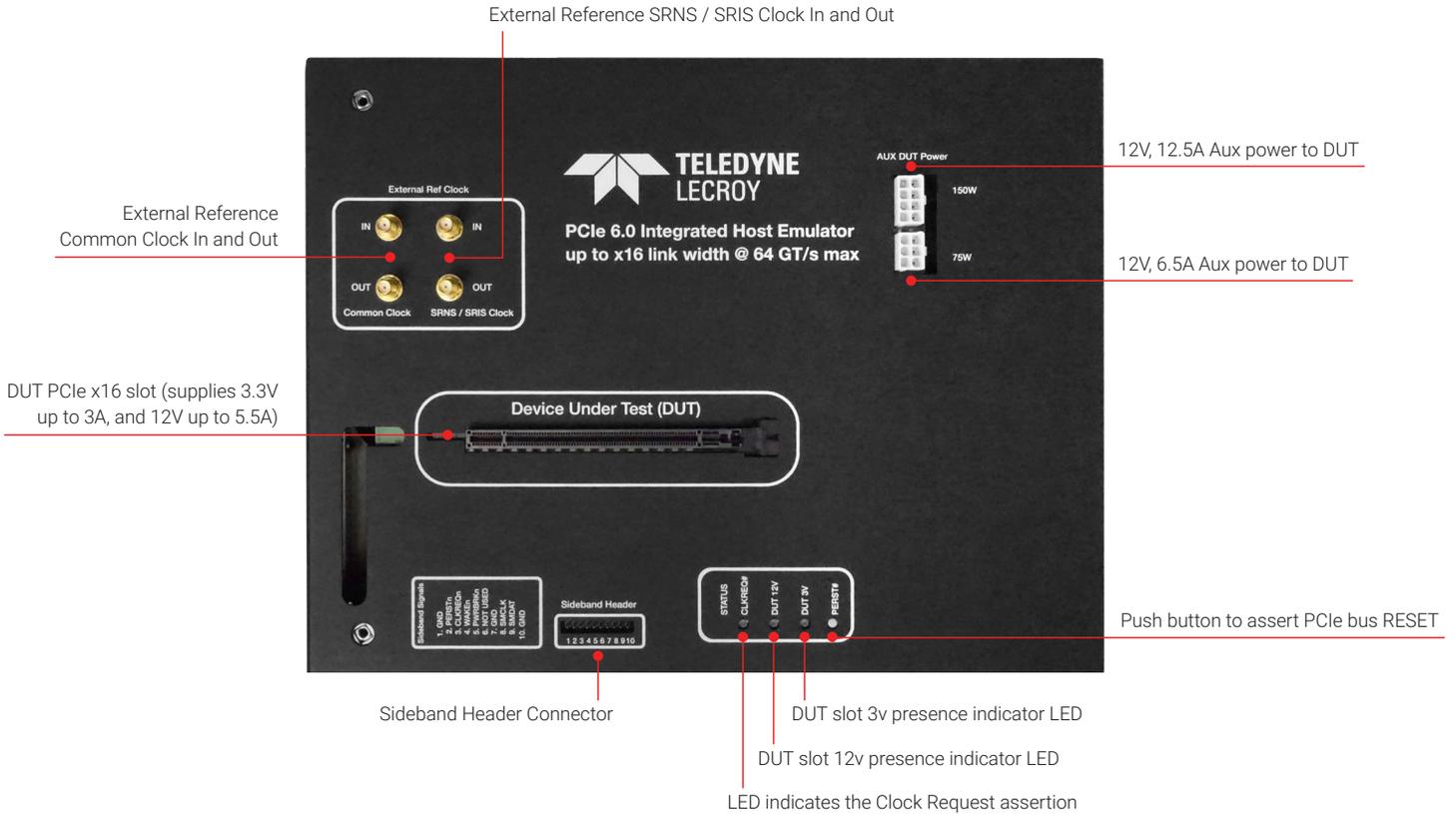
When the host emulator is used as a root complex, the Device Under Test (DUT) is plugged into the Host Emulator Platform PCIe x16 CEM slot and connected to the power source provided on the Host Emulator Platform.

When the host emulator is used as an endpoint the Summit M616 can emulate traffic through the CEM connector on the Host Test Platform as an endpoint. In this case, the DUT is a root complex plugged into the CEM slot on the Host Emulator and connected to the power source, enabling extensive protocol-level testing of PCIe root complex devices.

The user can select internal or external reference clocks separately for the Summit M616 as well as control power to the DUT. SMBus, CLKREQ# and SRIS signaling are supported on the test platform. An additional feature of the Host Emulator is the ability to generate power cycles to the DUT, useful in testing device management of power failure and re-initialization. The Host Emulator is an easy to use test platform for emulating PCIe devices at data rates up to 64 GT/s and with lane widths up to x16.

External Reference SRNS / SRIS Clock In and Out

External Reference Common Clock In and Out

12V, 12.5A Aux power to DUT

12V, 6.5A Aux power to DUT

DUT PCIe x16 slot (supplies 3.3V up to 3A, and 12V up to 5.5A)

Push button to assert PCIe bus RESET

Sideband Header Connector

DUT slot 3v presence indicator LED

DUT slot 12v presence indicator LED

LED indicates the Clock Request assertion

Ordering Information

Product Description	Product Code
PXM616 Protocol Exerciser x16 Host Emulation Platform	PE6001UEA-X