

AT A GLANCE

Subnanosecond time to digital converter for quantum research applications

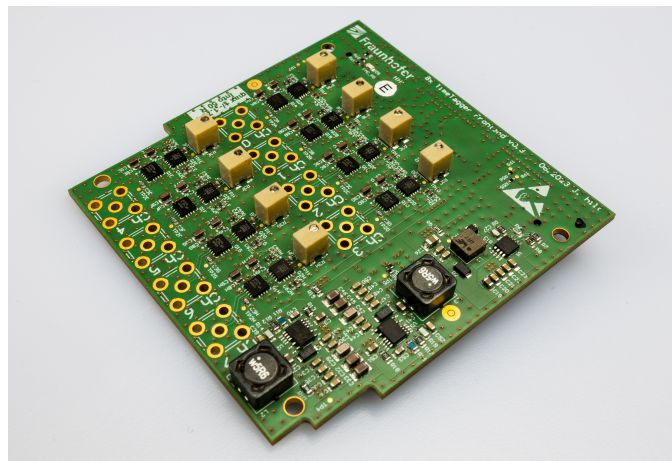
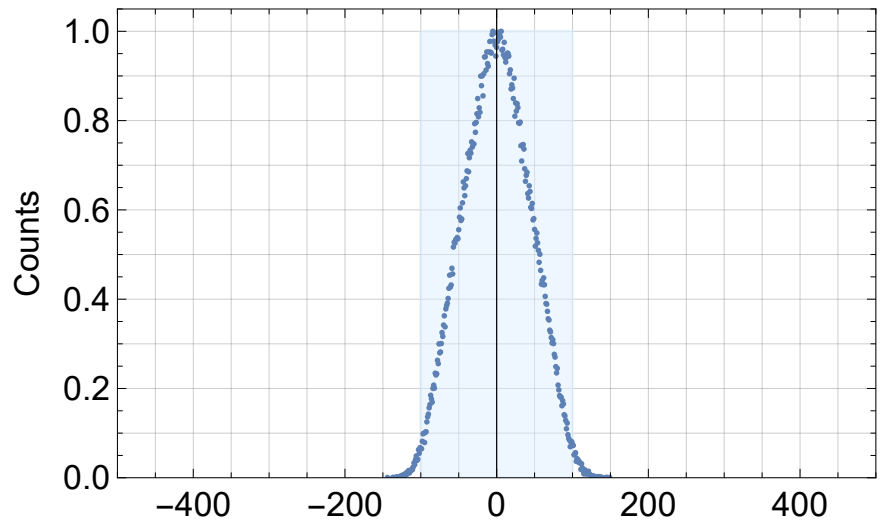
Benefits

- Low cost
- Easy integration
- No calibration necessary
- Stand alone OEM version available
- FMC card for high-end FPGA systems available

Background

HHI's Timetagger is a versatile measurement device with multiple input channels allowing digitizing the time of events with a resolution of 100 ps.

Switching threshold and hysteresis could be set for any input channel. The digitized events could be read by an ethernet interface or via high speed FMC interface for FPGA based applications.



Eight Channel Timetagger FMC Card

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Features

- 100 ps bin size
- Multiple input channels
- Adjustable threshold and hysteresis
- Ethernet Interface
- Full custom interface
- Available as measurement device or small footprint FPGA IP-Core

Applications

- Single Photon Counting
- Quantum Communications
- LIDAR
- Test and Measurements