

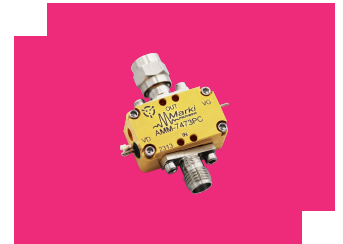
# AMM-7473PC

## 0.4 - 26.5 GHz GaAs MMIC Amplifier

### DEVICE OVERVIEW

#### General Description

The AMM-7473PC is a high-linearity broadband MMIC amplifier capable of providing +25 dBm output power typical. The AMM-7473PC can serve either as a linear signal amplifier, or as a saturated driver amplifier for H- or S-diode mixers. The amplifier has excellent return losses, gain flatness, and IP3.



#### Features

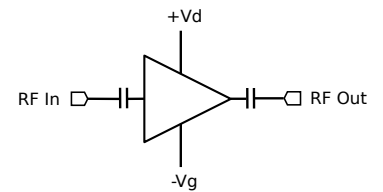
- +25 dBm output power up to 15GHz
- 16 dB gain up to 15GHz
- Excellent Return Losses
- No external bias tee required

#### Applications

- Mobile test and measurement equipment
- Radar and satellite communications
- Driver Amplifier for H and S - Diode Mixers

#### Functional Block

##### Diagram



#### Part Ordering Options

Part Number	Description	Package	Connectors	Green Status	Product Lifecycle	Export Classification
AMM-7473PC	0.4 - 26.5 GHz GaAs MMIC Amplifier	PC	<u>Standard</u>	REACH RoHS	Released	EAR99

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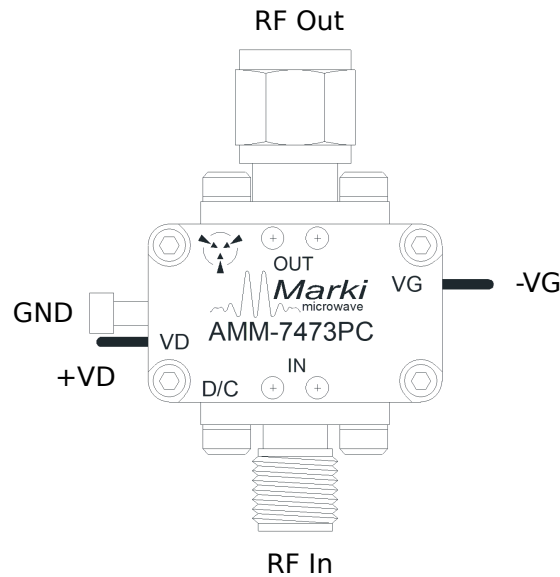
### Revision History

Revision Code	Revision Date	Comment
-	2023-04-01	Datasheet Initial Release

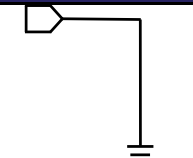
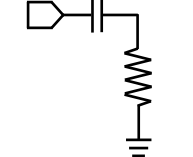
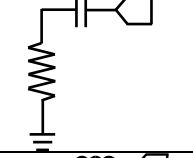
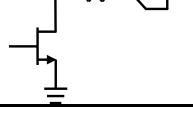
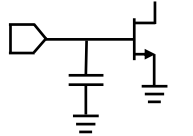
### Port Configuration and Functions

#### Port Diagram

A port diagram of the AMM-7473PC is shown below.



#### Port Functions

Port	Function	Connector Type	Description	Equivalent Circuit for Package
GND	Ground	-	Housing or ground lug must be connected to a DC/RF ground potential with high thermal and electrical conductivity.	
RF In	RF Input	SMAF	This is the RF Input port of the amplifier die. It is RF matched to 50 Ω and has built-in DC blocking capacitors.	
RF Out	RF Output	SMAM	This is the amplifier's RF Output. It is RF matched to 50 Ω and has built-in DC blocking capacitors. Must have less than 7:1 VSWR when operating.	
Vd	Drain Supply Pin	-	The VD pin supplies DC voltage to the drain of the amplifier IC. Apply gate bias voltage Vg before applying drain power supply.	
Vg	Gate Bias Pin	-	The VG pin provides a required negative bias which controls the drain power supply current to the amplifier. More negative voltage decreases the supply current. Apply gate bias voltage Vg before applying drain power supply.	

## Specifications

### Absolute Maximum Ratings

The Absolute Maximum Ratings indicate limits beyond which damage may occur to the device. If these limits are exceeded, the device may become inoperable or have a reduced lifetime. Reliability limits are individual, instantaneous catastrophic limits only. Functional operation limits are indicated below. Operation of the device at multiple absolute maximum limits or for extended periods at a single limit can cause degradation and damage to the device.

Parameter	Maximum Rating	Unit
Drain Current (RF Applied)	550	mA
Drain Supply Voltage	10	V
Gate Bias Voltage	0.5	V
Maximum Operating Temperature for MTTF > 1E6 hours	85	°C
Maximum Storage Temperature	150	°C
Minimum Operating Temperature for MTTF > 1E6 hours	-55	°C
Minimum Storage Temperature	-65	°C
RF Input Power	18	dBm

### Package Information

Parameter	Details	Rating
Weight	Package name: PC	11g
Dimensions	-	21.85 x 13.21 mm

### Recommended Operating Conditions

The Recommended Operating Conditions indicate the limits, inside which the device should be operated, to guarantee the performance given in Electrical Specifications. Operating outside these limits may not necessarily cause damage to the device, but the performance may degrade outside the limits of the electrical specifications. For limits, above which damage may occur, see Absolute Maximum Ratings.

Parameter	Min	Nominal	Max	Unit
Ambient Temperature	-40	25	85	°C
Power Supply DC Current (Id) (No RF Input) <sup>1</sup>	100	150	175	mA
Power Supply DC Voltage	5	7	7.5	V
Input Power for Saturation	11	15	16	dBm

<sup>[1]</sup> Recommended operating current conditions without RF input applied.

### Sequencing Requirements

Turn-on Procedure if required biases are unknown:

1. Apply -1 V to Vg.
2. Apply desired Vd.
3. Increase Vg voltage towards -0.5 V until Id = 150 mA.
4. Apply RF input power.

Turn-on Procedure if required biases are known:

1. Apply desired Vg (previously determined to produce 150 mA Idq).
2. Apply desired Vd.
3. Apply RF input power.

Turn-off Procedure:

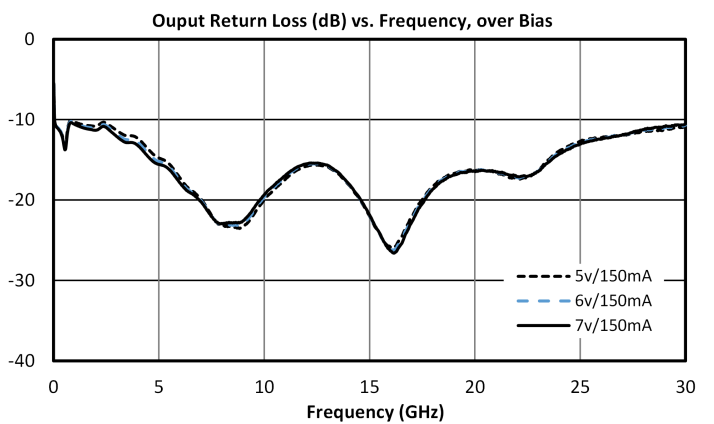
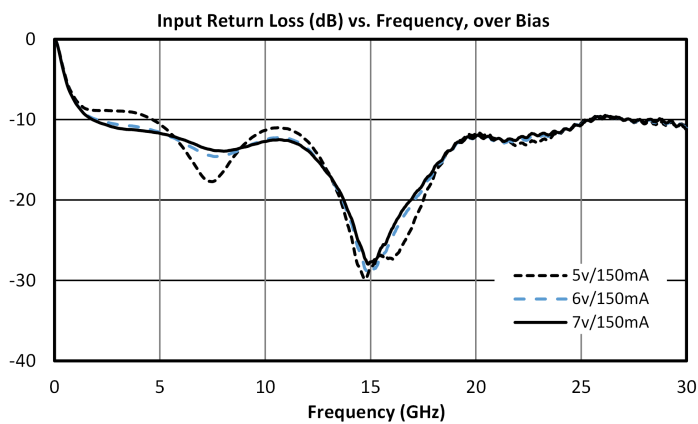
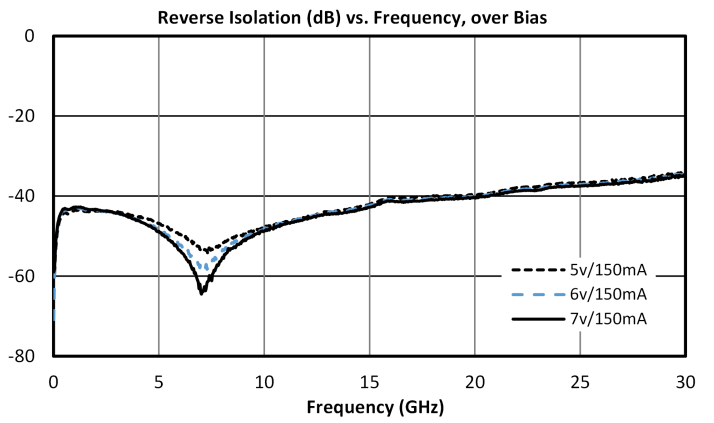
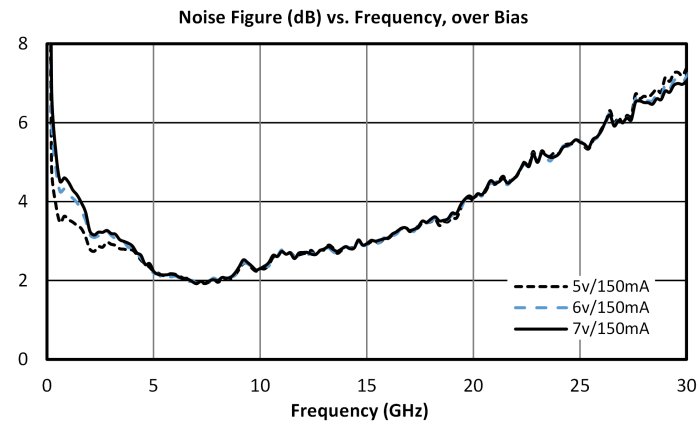
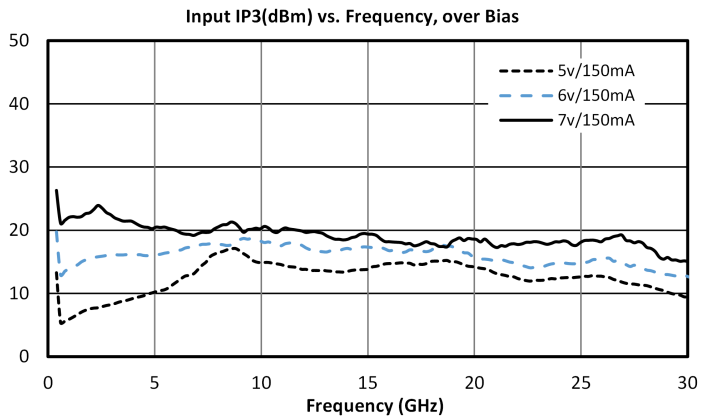
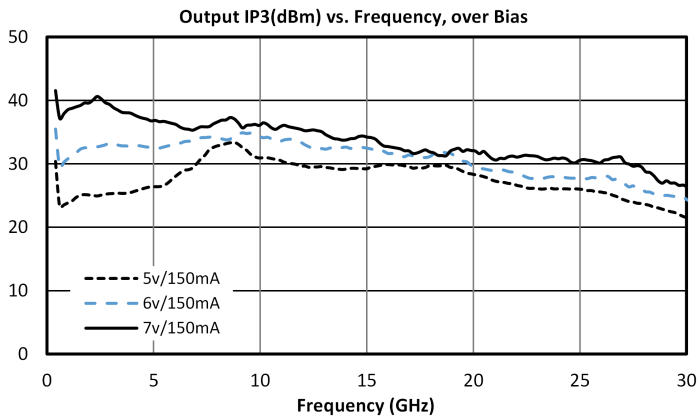
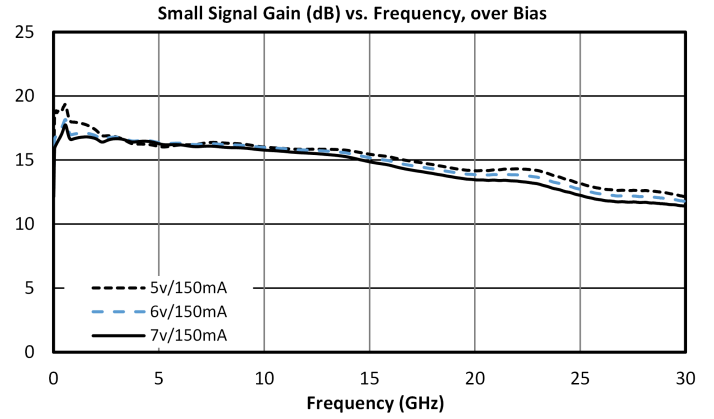
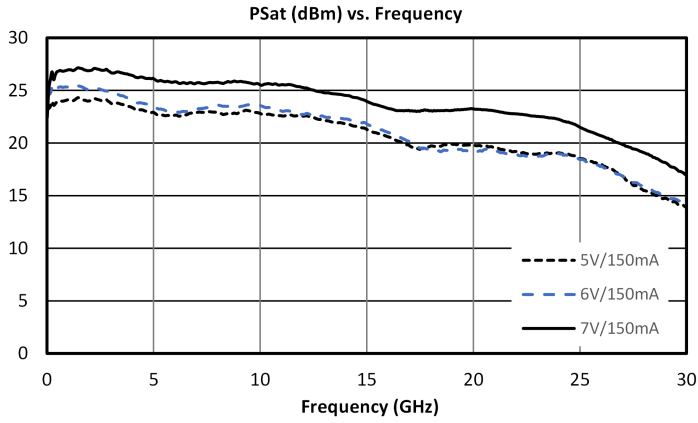
1. Turn off RF input power.
2. Turn off Vd.
3. Turn off Vg.

## Electrical Specifications

Unless otherwise specified, electrical specifications apply at TA=+25°C, Vd = 7V, Idq = 150mA (where Idq is the drain current with no RF applied). Vg is set as required to achieve Idq = 150mA in a 50Ω system. Min and Max limits apply only to our connectorized units and are guaranteed at TA=+25°C

Parameter	Test Conditions	Minimum Frequency (GHz)	Maximum Frequency (GHz)	Min	Typ	Max	Unit
Small Signal Gain	Vd = 7V, Pin = -20 dBm	0.4	15	14	16	-	dB
Small Signal Gain	Vd = 7V, Pin = -20 dBm	15	26.5	11	13	-	dB
Noise Figure	Vd = 7V, Pin = -20 dBm	2	5	-	3.4	-	dB
Noise Figure	Vd = 7V, Pin = -20 dBm	5	10	-	2.3	-	dB
Noise Figure	Vd = 7V, Pin = -20 dBm	10	26.5	-	3.9	-	dB
Saturated Output Power	Vd = 7V	0.4	15	23	25	-	dBm
Saturated Output Power	Vd = 7V	15	26.5	18	21	-	dBm
Output IP3	Vd = 7V, Pin = -15 dBm per tone, 10 MHz tone spacing	0.4	26.5	-	34	-	dBm
Input IP3	Vd = 7V, Pin = -15 dBm per tone, 10 MHz tone spacing	0.4	26.5	-	16	-	dBm
Input Return Loss	Vd = 7V, Pin = -20 dBm	0.4	26.5	-	10	-	dB
Output Return Loss	Vd = 7V, Pin = -20 dBm	0.4	26.5	-	15	-	dB
Reverse Isolation	Vd = 7V, Pin = -20 dBm	0.4	26.5	-	40	-	dB
Input Power for Saturation	Vd = 7V	0.4	26.5	11	15	-	dBm
DC Supply Quiescent Current (Idq)	Vd = 7V, no RF input	-	-	-	150	-	mA

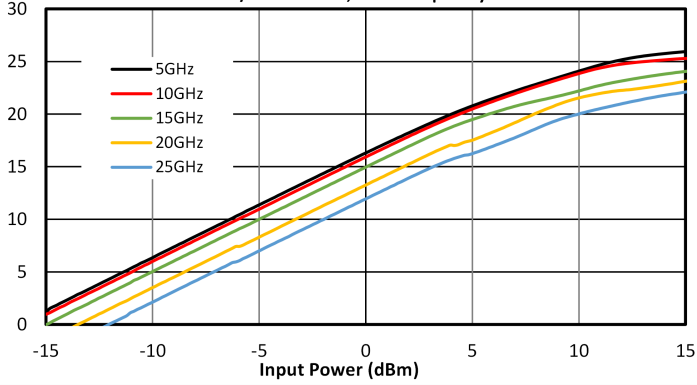
### Typical Performance Plots



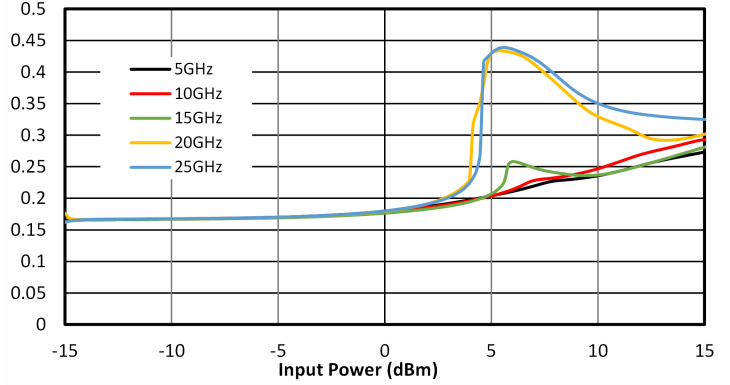
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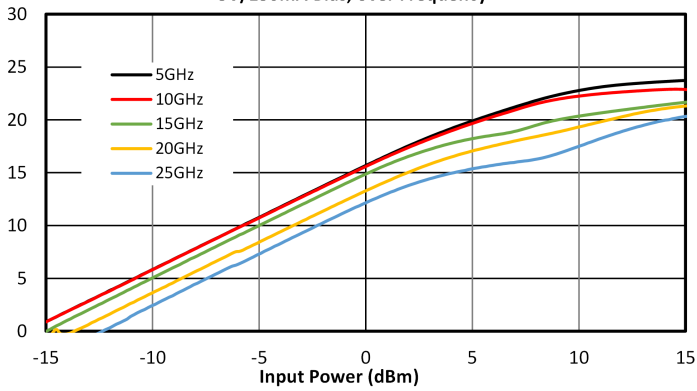
Output Power (dBm) vs RF Input Power  
7V/150mA Bias, over Frequency



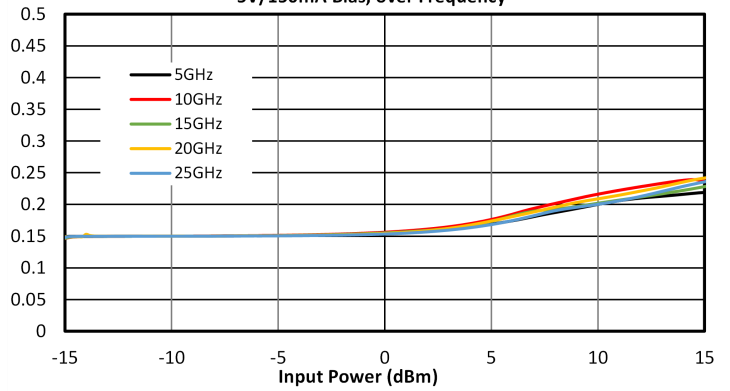
Id (A) vs RF Input Power  
7V/150mA Bias, over Frequency



Output Power (dBm) vs RF Input Power  
5V/150mA Bias, over Frequency



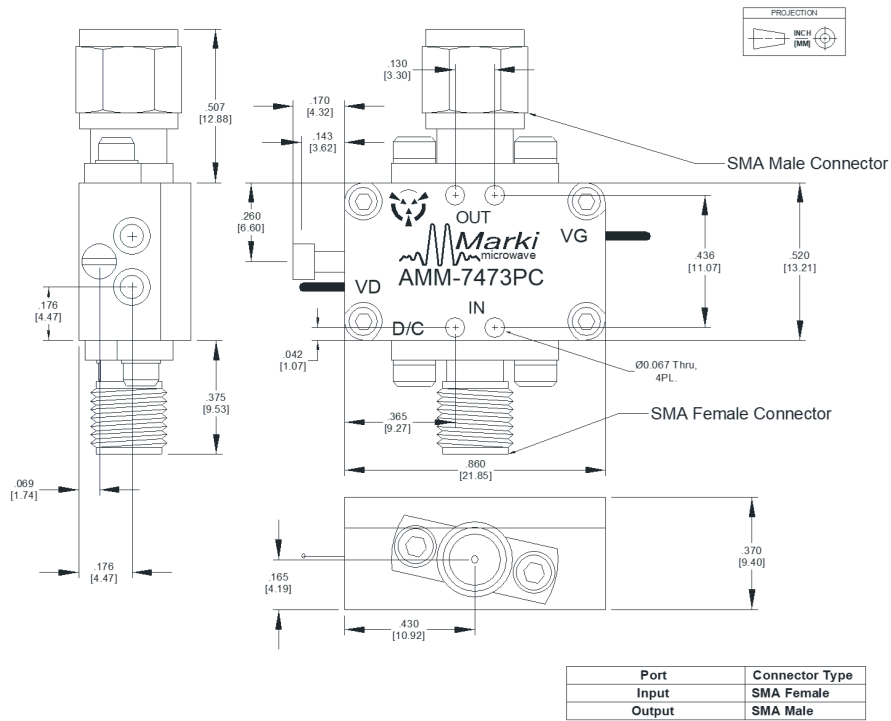
Id (A) vs RF Input Power  
5V/150mA Bias, over Frequency



### Mechanical Data

### Outline Drawing

Download : [Outline 3D Drawing](#) | [Outline 3D STP](#)



### Package Notes:

- 1) All measurements are typical.
- 2) Ground lug and bias pins are solderable.



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