

## 1 Purpose

WORK phase-locked oscillator (PLO) products are rugged modular components that can be used in a wide variety of applications. The phase-locked oscillators have found their way into every type of high quality telecommunications, lab testing, satellite up- and downconverters, radar and many other applications that require the high quality and performance that we design into our products.

## 2 Performance Parameters

The phase-locked frequency source is both an accurate and stable signal source. Its stability and accuracy are directly related to the stability and accuracy of a lower-frequency reference. The most significant parameters are the frequency band of operation and the corresponding frequency resolution. Next would be performance requirements such as phase noise, spurious, harmonics, type of reference, operating voltage and power consumption. The PLO-CRO-xxxx.x-XO Series is designed as a multi loop, single frequency, very low phase noise device. It comes with an internal crystal reference. Typical performance:

- Low phase noise design
- Medium output level, typical +10 dBm
- Low spurious and harmonics
- Low power consumption < 3 W
- Frequency stability < 100 ppm over temperature and aging over 15 years
- Wide operating temperature range -30°C ... +70°C

## 3 Setup

WORK synthesizers are compatible with many standard serial or parallel interfaces to program frequency, and can be customized to meet any user protocol. Standard user interfaces can be downloaded from the WORK web site and operate from any windows based PC.



Figure 1: PLO-CRO-xxxx.x-XO product example.

#### 4 Dimensions of PLO-CRO-xxxx.x-XO Series

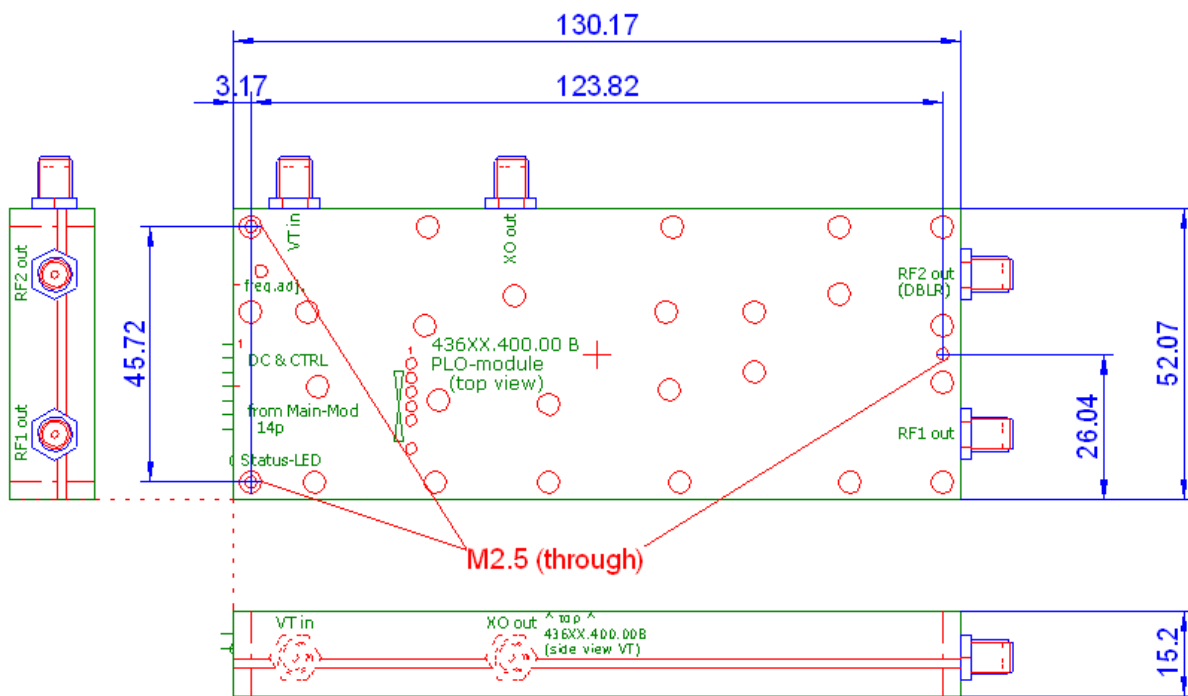
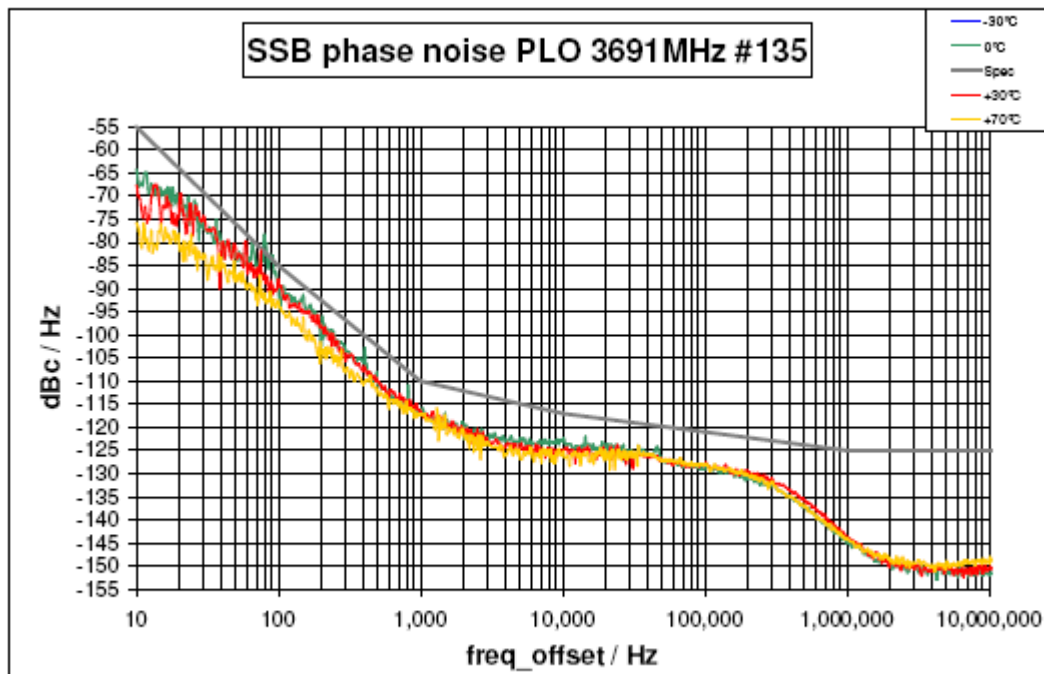


Figure 2: Outline Dimensions.

## 5 Technical data

<b>PLO Type:</b>	<b>PLO-CRO-xxxx.x-XO</b>		
<b>RF-Output Frequency:</b>	1200.0 ... 9999.9 MHz		
<b>Phase Noise: (3691 MHz)</b>	10 Hz	-55	-60
	100 Hz	-85	-90
	1 kHz	-110	-115
	10 kHz	-117	-124
	100 kHz	-121	-127
	1 MHz	-125	-144
	10 MHz	-125	-148
	max. values in dBc/Hz		typ. values in dBc/Hz
<b>Spurious Outputs:</b>	$\Delta f < 10$ MHz:	< -60 dBc	
	$\Delta f > 10$ MHz:	< -70 dBc	
	Output harmonics:	< -50 dBc	
<b>Output level:</b>	Connector:	+10 dBm $\pm$ 2 dB SMA (female)	
<b>Frequency stability:</b>	-20°C ... +70°C and aging	< $\pm$ 100 ppm	
<b>Lock detect output:</b>	TTL, active high		
<b>Temperature Range:</b>	-30 °C ... +70 °C operating, -40 °C ... 80 °C storage		
<b>Relative Humidity:</b>	< 95 % non condensing		
<b>Power Input:</b>	7 V $\pm$ 5%, <430 mA		
<b>Power Consumption:</b>	max: 3 W		
<b>Power and control connector:</b>	14-Pin-header, male		
<b>Dimension and Weight:</b>	130.2 x 52.1 x 15.2 mm <sup>3</sup> (WxHxD), approx. 135 g		

Specifications are subject to change



#135 Diagramm 1

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Figure 3: Typical phase noise over temperature.

## 6 Ordering information

<b>Model-Nr</b>	<b>Description</b>	<b>Connector</b>
PLO-CRO-xxxx.x-XO	PLO, xxxx.x = frequency in MHz	SMA

## 7 Company address

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