Single Chip
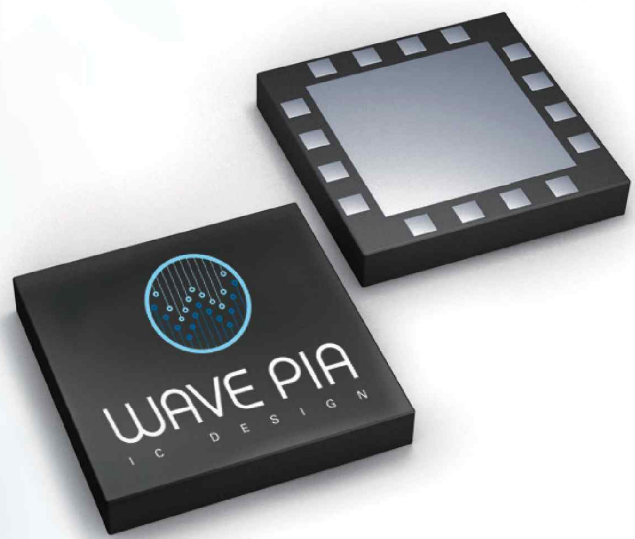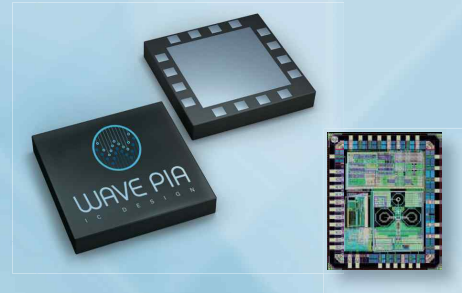# Multi-Clock
CMOS Oscillator

WAVE PIA
IC DESIGN

Single Chip Multi-Clock CMOS Oscillator

Product

Part name	VDD	IDD	Stability	Remark
ANYAN-S30X	2.5 V	< 10mA	< ±40 ppm	3 x 3 mm QFN 16P
ANYAN-S40X	2.5 V	< 10mA	< ±40 ppm	4 x 4 mm QFN 20P
ANYAN-I0X	2.5 V	< 10mA	< ±40 ppm	IP- Version (TBD)
ANYAN-DS4	2.5 V	< 10mA	< ±40 ppm	5 x 5 mm QFN 32P
ANYAN-DI4	2.5 V	< 10mA	< ±40 ppm	IP- Version (TBD)

*ppm : parts per million
* X : 1. Single output oscillator with OE
     2. Dual output oscillator with OE and FS
     3. Quad output oscillator with OE and FS
     4. Programmable oscillator with OE and FS for PPB
*OE : Output Enable, FS: Frequency select, PPB: Part per billion
*Sample available at the end of March, 2016

Frequency (MHz)	1	2	3	4	5	6
	0.032768	8	13	19.2	26	32
	7	8	9	10	11	12
	38.4	44	100	125	156.25	312.5

Features

- Reference Free clock generator ; Fully CMOS without external clock
- Replaceable the crystals (between 32.768kHz and 312.5MHz or an external clock)
- Reduced crystal cost
- Reduced board (PCB) area to support mobile wearable device
- Reduced unwanted spurs (EMI) ; too many reference clocks may de sense the wanted signal
- Reduced time to market
- Increased integration by supporting multiple references
- Support I2C Interface

Application

- Universal purposes
- Bluetooth system, BLE, NFC, RFID, DMB, TPMS
- USB3.0/3.1, PCIE 3.0/4.0, DP, HDMI
- Wearable device, Healthcare, Sensors