

Express Converter

Hi-Speed Digitizer (DAQ) Series



APX-5200B-1

12Bit/1GSps 2Ch(Sync) hi-speed sampling Digitizer

- › High speed ADC with 12 bit / 1 GSps sampling
- › A single end input of ± 0.5 V is connected to 2 ch
- › 2ch synchronous sampling
- › Compatible with 2GSps, 12bit, 1Ch interleave mode
- › 2Ch analog output (Arbitrary no. of point with PC memory)
- › External trigger input · External clock input for each 1 channel
- › General purpose DI · DO for each 4 ch
- › DDR3-SDRAM (8GByte) is installed as FIFO Memory
- › Customization of FPGA processing available
- › PCI Express 2.0 (Gen 2) 5.0 GT / s × 8 compatible



Specifications

Model	APX-5200B-1
Input channel	Single end 2 ch (interleaved: single end 1 ch)
resolution	12bit
Sampling clock	Frequency range: 150 MSps to 1 GSps (interleave: 2 GSps) Frequency Resolution: Individual (* 1, * 2)
Sampling time	1 ns (min) (at interleave: 0.5 ns (min))
Number of samples	2 G words / 2 ch, 4 G words / 1 ch

Input range	±0.5V
Input impedance	50Ω
Analog band	900MHz(-3dB)
Input connector	SMA connector
Trigger	External trigger / analog trigger / soft trigger
External clock	Input (when PPL is used): 5 MHz to 100 MHz Input (direct input): 500 MHz to 1000 MHz
External trigger	Input: LVTTTL edge detection (invertible)
SNR	59.41dB
SINAD	59.17dB
ENOB	9.53bit
SFDR	72.18dB
memory	DDR3 – SDRAM (8 GB)
FPGA	5 SGXMA 3 K 1 F 40 C 2 N (manufactured by ALTERA) Trigger type: Internal trigger (analog trigger), external trigger, soft trigger Analog trigger: edge · pulse trigger position: pre / post delay
General purpose AO	Number of outputs: 2 ch Resolution: 16 bits Output range: -4 V to +4 V
System bus	PCI Express 2.0 (Gen 2) 5.0 GT / s × 8
Power supply	+ 12 V / 3.3 A (39.6 W)
Operating environment	Temperature 0 to 45 ° C, humidity 35% to 85% (no condensation)
Size	212.5 mm × 111.15 mm, panel width 20 mm (not including projections)
weight	
Supported OS	Windows 7/8/10 compatible with 32 bit / 64 bit each
Software development kit	Windows: AWP-ADCSDK2-01 Linux: ALP-5200-01
Evaluation tool	AWP-ADCTOOL-01
LabView VI Sample	AWP-ADCLV-01
Environmental response	RoHS

* 1: On board, default clock is set as 25MHz

* 2: There is a limit to the sample rate that can be set:※ This product cannot be recognized correctly by some Workstation (PC) equipped with Xeon E5 V3 series processor.

※ For details please contact us: sales@aval-global.com(Notes)

– The specifications and appearance stated are subject to change without notice.

– Power consumption does not include inrush current.

– In order to use the product correctly, be sure to read the instruction manual before using the product. Also, use the product under the conditions applicable to your product warranty.

Panel

Application

software tool

Customization

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