





## APX-5360G3A

Resolution 12bit, 3.6 GSps high-speed sampling A/D Conversion Board

- PCI Express 3.0 (Gen 3) x 8 enables high-speed data transfer
- DDR3 memory equipped, up to 4G point size data transferable
- External trigger input · External clock input for each 1 CH
- Equipped with AD capable of up to 1.8 GSps
- ± 0.5 V Single Ended Input 2 CH
- OFFSET adjustment function
- 4 CH LVTTL level D-IO function (5V tolerant)
- AO function of 2 CH
- RoHS compliant





## **Specifications**

| Model#       |                 | APX-5360G3A                                    |
|--------------|-----------------|------------------------------------------------|
| Analog I / F | Input           | Single end 2 ch (interleaved: single end 1 ch) |
|              | Input impedance | 50Ω                                            |
|              | Coupling        | DC Coupling                                    |
|              | Input range     | ±0.5V                                          |
|              | Resolution      | 12bit                                          |
|              | Sample rate     | Maximum 1.8 GSps / Interleaved: 3.6 GSps       |

|                                                 | Frequency band       | 900MHz-3dB                                                                                 |
|-------------------------------------------------|----------------------|--------------------------------------------------------------------------------------------|
|                                                 | Input maximum rating | ±2 V (when energized) ±1.5 V (when no energized)                                           |
|                                                 |                      | SFDR: 68.0dB@1800MSps, Fin1MHz                                                             |
|                                                 | Analog performance   | SNR:56.3dB@1800MSps, Fin1MHz                                                               |
|                                                 |                      | ENOB: 9.0bit@1800MSps, Fin1MHz                                                             |
| External clock input                            |                      | Number of channels: 1                                                                      |
|                                                 |                      | Signal level: 0.4 Vp - p to 1.5 V p - p                                                    |
|                                                 |                      | (sine wave / square wave)                                                                  |
|                                                 |                      | Frequency: 5 MHz to 100 MHz (when PLL is used)                                             |
|                                                 |                      | 150 MHz to 1800 MHz (at the time of direct sampling)                                       |
|                                                 |                      | Input impedance: $1 M\Omega / 50 \Omega$ (AC coupling)                                     |
|                                                 |                      | Connector shape: SMA                                                                       |
|                                                 |                      | Number of channels: 1                                                                      |
|                                                 |                      | Signal level: LVTTL                                                                        |
| EXI                                             | ernal trigger input  | Input impedance: 1 K $\Omega$ / 50 $\Omega$                                                |
|                                                 |                      | Connector shape: SMA                                                                       |
| General purpose port: DIO                       |                      | Number of channels: DI 4ch DO 4ch                                                          |
|                                                 |                      | Signal level: LVTTL                                                                        |
|                                                 |                      | Connector Model name: HDR – EC 26 LFDT 1 – SLD +                                           |
| Analog output                                   |                      | Number of channels: 2                                                                      |
|                                                 |                      | Resolution: 16 bits                                                                        |
|                                                 |                      | Output range: ±4V                                                                          |
| Trigger                                         |                      | External trigger / analog trigger / soft trigger Analog trigger: edge · pulse trigger posi |
|                                                 |                      | pre / post delay                                                                           |
| Maxim                                           | um number of samples | 2 G words / 2 ch, 4 G words / 1 ch                                                         |
|                                                 | Memory               | DDR3 – SDRAM (8 GB)                                                                        |
| System bus                                      |                      | PCI-Express3.0(Gen3)8.0GT/s×8                                                              |
| Power supply                                    |                      | +12V±8%, +12V/3.4A(40.8W)                                                                  |
| Оре                                             | erating environment  | Temperature 0 to 50 ° C, humidity 35% to 85% (no condensation)                             |
|                                                 | FPGA                 | 5 GXMA 3 K 1 F 40 I 2 N (manufactured by ALTERA)                                           |
| External dimensions (not including projections) |                      | 220.15 mm x 106.65 mm, panel width 20 mm                                                   |
|                                                 | Supported OS         | Windows 7/8/10, Linux compatible with 32 bit / 64 bit                                      |
|                                                 |                      |                                                                                            |

## Notes)

- The specifications and appearance stated are subject to change without notice.
- Power consumption does not include inrush current.
- In order to use the product correctly, be sure to read the instruction manual before using the product. Also, use the product under the conditions applicable to your product warranty

| Panel         |
|---------------|
| Software tool |
| Customization |

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